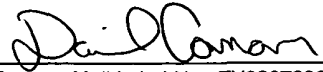


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PARALLEL, INDIVIDUALLY ADDRESSABLE PROBES FOR NANOLITHOGRAPHY

1 RELATED APPLICATIONS

2 This is a continuation of application Serial No. 10/008,719, filed
3 December 7, 2001, which claims priority of U.S. Provisional Application Serial
4 No. 60/307,976, filed July 26, 2001.

STATEMENT OF GOVERNMENT INTEREST

5 This invention was made with United States Government
6 assistance through Defense Advanced Research Projects Agency (DARPA)
7 Contract No. NW 0650 300 F 245. The Government has certain rights in this
8 invention.

FIELD OF THE INVENTION

9 The present invention relates generally to the art of efficient and
10 high-speed generation of fine surface patterns made of chemical resists or
11 biological substances using micromachined or microfabricated probes.

BACKGROUND OF THE INVENTION

12 High-throughput lithography and surface patterning with
13 extremely fine linewidths (e.g., on the order of 10-100 nm) are very important
14 for the future growth of the microelectronics industry and nanotechnology.

1 Next-generation integrated circuit technology will inevitably call for efficient
2 and low-cost generation of features with a sub-100-nm linewidth. The
3 emerging field of nanotechnology also requires patterning and functionalization
4 of surfaces with a spatial resolution that is comparable with the scale of the
5 molecules and cells that need to be manipulated and modified.

6 The resolution of conventional projection optical lithographic
7 systems, still the most widely used in the microelectronics industry, is limited
8 by optical diffraction. The resolution can be improved by using beam-based
9 direct-writing tools with high energy and short wavelengths. High-energy
10 beam lines, including ones that rely on electron beams and X-rays, are being
11 used. However, such direct-write lithography systems suffer from several
12 drawbacks. First, such systems are invariably complex and expensive.
13 Second, these lithographic tools operate with a single beam and produce
14 patterns in a serial manner, resulting in low throughput. Third, conventional
15 high resolution lithography systems are not capable of depositing patterns made
16 of biological molecules or chemical compounds. Only special chemical resists
17 may be used.

18 Dip-pen Nanolithography (DPN) is a new and recently
19 introduced method of scanning probe nanolithography. A description of DPN
20 is contained in PCT/US00/00319, the entirety of which is incorporated herein
21 by reference. It functions by depositing nanoscale patterns on surfaces using
22 the diffusion of a chemical species from a scanning probe tip to the surface,
23 sometimes via a water meniscus that naturally forms between tip and sample

1 under ambient conditions. As a DPN tip is scanned across the surface of a
2 substrate, molecules on the surface of the tip are transported through the water
3 meniscus that forms between the tip and the substrate surface. Once on the
4 surface, the molecules chemically anchor themselves to the substrate, forming
5 robust patterns. Features in the 10 nm to many micrometer range can be
6 fabricated with commercially available silicon nitride tips. One factor that
7 influences the linewidth of DPN writing is the linear speed of the tip. Smaller
8 linewidths are achieved with faster tip speeds. Other factors that influence the
9 linewidth include the sharpness of the DPN tip and the diffusion constants of
10 the molecules used as inks.

11 DPN offers a number of unique benefits, including direct writing
12 capability, high resolution (~ 10nm linewidth resolution, ultimate ~ 5 nm
13 spatial resolution), ultrahigh nanostructure registration capabilities, the
14 flexibility to employ a variety of molecules for writing compounds (including
15 biomolecules) and writing substrates (such as Au, SiO₂, and GaAs), the ability
16 to integrate multiple chemical or biochemical functionalities on a single “nano-
17 chip”, a one-layer process for patterning, and the ability to automate patterning
18 using customized software.

19 DPN technology can be implemented using a low-cost
20 commercial scanning probe microscope (SPM) instrument. In a typical setup,
21 the DPN probe chip is mounted on an SPM scanner tube in a manner similar to
22 commercially available SPM tips. Precise horizontal and vertical movement of

1 the probes is attained by using the internal laser signal feedback control system
2 of the SPM machine.

SUMMARY OF THE INVENTION

3 The present invention provides nanolithography, such as Dip Pen
4 Nanolithography, as well as nanoscale imaging, with individually addressable
5 probes in dip pen arrays. A probe array having a plurality of active probes is
6 provided, which allows greater functionality than in conventional, single-pen
7 DPN by allowing independent actuation of individual probes through supplying
8 current or voltage to an actuator coupled with the probe. A plurality of
9 independently addressable probes produces a plurality of traces of same or
10 different chemicals.

11 An apparatus is provided for applying at least one patterning
12 compound to a substrate for nanolithography. The apparatus includes an array
13 of parallel probes, each probe including a cantilever, a tip at a distal end of the
14 cantilever for applying one of the at least one patterning compound to the
15 substrate, and an actuator operatively coupled to the cantilever. The probes
16 may be configured for Dip Pen Nanolithography. The actuator is designed to
17 be responsive to an applied current or voltage to move the cantilever, and thus
18 move the tip away from the substrate. The contact state between individual
19 probe tips and the writing substrate can thus be independently controlled. In
20 the case of DPN writing, the patterning process is suspended when the probe

1 tip leaves the substrate. A number of preferred types of embodiments are
2 disclosed. Methods are also provided for fabricating active probe arrays.

3 In one preferred type of embodiment of the invention, the
4 actuator deflects the cantilever in response to applied electrical current to move
5 the tip relative to the substrate. The actuator may be thermally operated.

6 According to a preferred embodiment, a thermal actuator includes
7 a resistive heater connected to the cantilever and a wire connecting the resistive
8 heater to a current source. When a current is applied through the resistive
9 heater, heat is generated due to ohmic heating, thus raising the temperature of
10 the resistor as well as the cantilever. Due to difference in the thermal
11 expansion coefficient of the materials for the cantilever and for the metal
12 resistor, the cantilever will be bent selectively in response to the applied
13 current. A patch of thin metal film can be connected to the cantilever for
14 enhancing the extent of thermal bending.

15 In a second type of preferred embodiment of the invention, the
16 actuator deflects the cantilever in response to applied voltage. The actuator
17 may be electrostatically operated. Preferred displacement is created by
18 applying a voltage differential between two electrodes, at least one of them
19 being not stationary.

20 A preferred embodiment of an electrostatic actuator includes a
21 paddle electrode formed at an inner end of the cantilever opposite to the tip and
22 a counter electrode. The paddle electrode faces the counter electrode with a
23 gap having a predefined gap spacing. When a differential electrical voltage is

1 applied across the top electrode and the counter electrode, the resultant
2 electrostatic attraction force bends the cantilever beam and therefore moves the
3 tip positions.

4 A preferred type of method of the current invention provides a
5 method for applying at least one patterning compound to a substrate for high-
6 speed probe-based nanolithography. The method includes the steps of:
7 providing an array of individually addressable probes, each probe having a tip
8 on a distal end; coating tips with same or different chemical substances;
9 positioning the tips of the array of individually addressable probes over the
10 substrate so that the tips are in contact with the substrate; raster-scanning the
11 probes over the substrate surface; and selectively actuating at least one selected
12 probe from the array of probes to move the tip of the selected probe away from
13 the substrate. Accordingly, the selected probe does not apply patterning
14 compound to the substrate when selected, while the non-selected probes apply
15 at least one patterning compound to the substrate. Arbitrary two-dimensional
16 patterns can be produced by raster-scanning the chip that contains the arrayed
17 probes while controlling the position of individual probes during the scanning
18 process. The probes may be configured for Dip Pen Nanolithography. The
19 probes can also be generally applied to other nanolithography techniques where
20 the interaction between a tip and a substrate alters the electrical, chemical, or
21 molecular state of the surface, and may be used for imaging.

22 According to a preferred method of the present invention, the step
23 of selectively actuating at least one selected probe includes the step of applying

1 a current to a resistive heater connected to the cantilever, so that the cantilever
2 beam is flexed. The deflection of the cantilever moves the tip away from the
3 substrate to suspend writing on the substrate.

4 According to another preferred method of the present invention,
5 the step of selectively actuating an individual probe includes applying a
6 differential electrical voltage across a counter electrode and a moving electrode
7 connected to an end of the selected probe. In this way, the moving and counter
8 electrodes are moved towards one another, preferably to deflect the cantilever
9 of the probe and move the tip away from the substrate.

10
11

BRIEF DESCRIPTION OF THE DRAWINGS

12

13 FIG. 1 is a schematic representation of the DPN process, showing
14 a single tip coated with chemical compounds passing over a substrate (writing
15 surface);

16 FIG. 2 is a schematic diagram of a parallel nanolithography
17 writing system having a probe array according to one type of embodiment of
18 the present invention, interfaced with an auxiliary control unit;

19 FIGs. 3a-3b are schematics of an array of bimetallic thermally
20 actuated probes before and after deflection of selected probes, respectively,
21 according to a preferred type of embodiment of the present invention;

22 FIGs. 4a-4b are schematics of a bimetallic thermally actuated
23 probe before and after deflection of the probe, respectively;

1 FIGs. 5a-5e are schematic drawings showing major steps in the
2 fabrication process of a thermally actuated probe according to a preferred
3 aspect of the invention;

4 FIGs. 6a-6d are schematic drawings showing a top view of the
5 fabrication steps shown in FIGs. 5b-5e, respectively;

6 FIG. 7 is a schematic drawing of an electrostatically actuated
7 probe according to a preferred type of embodiment of the invention;

8 FIG. 8 is a schematic drawing of an array of electrostatically
9 actuated probes according to a preferred type of embodiment of the invention;

10 FIG. 9 is a schematic showing a top view of an electrostatic
11 actuator probe;

12 FIGs. 10a-10f are schematics taken along a section of FIG. 9 and
13 in the direction indicated, showing fabrication steps for an electrostatically
14 actuated probe according to a preferred method of the invention; and

15 FIG. 11 is a schematic drawing of a two-dimensional array DPN
16 nanoplotter according to another preferred embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

17 Generally speaking, the present invention provides active probes
18 and active probe arrays, which are designed to achieve direct-write
19 nanolithography, such as DPN. Devices according to the present invention can
20 generate sub-100nm patterns in a high speed, parallel, and controllable fashion.
21 The active probe arrays offer greater functionality by allowing actuation of

1 individual probes through supplying current or voltage to an actuator of the
2 probe. The present invention is primarily directed to methods and devices for
3 parallel DPN using active probe arrays, and methods for fabricating active
4 probes and active probe arrays.

5 The active probe array can also be used for other existing or
6 future surface patterning and lithography methods based on the scanning probe
7 microscope (SPM) instrument family. An atomic force microscope (AFM) is
8 considered a member of the SPM instrument family. Examples of such
9 lithography systems include local thermal oxidation and displacement
10 lithography.

11 Referring now to FIG. 1, an example of a conventional DPN
12 process is shown. DPN employs a tip 20 on a distal end of a cantilever of an
13 AFM probe 22 (or other SPM probe) to deposit, or “write”, nanoscale patterns
14 onto a solid writing substrate 24, such as gold. The tip 20 applies a patterning
15 compound 26 coated on the tip 20 to the writing substrate 24. The patterning
16 compound 26 may be a hydrophobic patterning compound with a chemical
17 affinity for the writing substrate 24, such as, but not limited to, 1-
18 octadecanethiol (ODT) or mercaptohexadecanoic acid (MHA).

19 Similar to traditional macroscopic “dip pens” (*e.g.*, quill,
20 fountain, or ball-point pens, or multi-pen plotters), DPN employs molecular
21 (capillary) transport to transfer the patterning compound 26 from the tip 20 to
22 the writing substrate 24, forming a pattern 28 of the patterning compound. A
23 water meniscus 30 forms between the tip 20 and the writing substrate 24 due to

1 relative humidity in a work area, and carries the patterning compound 26 from
2 the tip to the writing substrate as the tip is moved relatively to the writing
3 substrate in the direction of the writing W, as indicated on FIG. 1.

4 Initial DPN processes involved a single probe 22 (pen). Parallel
5 patterns also have been realized using an array of up to eight commercial
6 probes 22 with an inter-probe spacing of 1.4 mm to write a plurality of patterns
7 28 on the writing substrate 24. This technique also allows application of
8 multiple patterns 28, where each pattern contains a different patterning
9 compound, such as a biocompound. Parallel writing is also useful, for
10 example, to form patterns 28 during integrated circuit formation. Examples of
11 parallel probe structures can be found in R. Piner *et al.*, "Dip-Pen"
12 Nanolithography, Science, 1999, v. 283, pp. 661-663; S. Hong *et al.*, Multiple
13 Ink Nanolithography: Toward a Multiple-Pen Nano-Plotter, 1999, v. 286, pp.
14 523-525; S. Hong *et al.*, A Nanoplotter with Both Parallel and Serial Writing
15 Capabilities, Science, v. 288, pp. 1808-1811.

16 Conventional parallel probe DPN processes are performed using
17 commercially available AFM probes 22. Individual probes 22 cannot be
18 moved independently from one another. Hence, all probes 22 must move
19 simultaneously. Also, the inter-probe spacing of current parallel DPN arrays is
20 too large for certain DPN applications and cannot fully satisfy the needs for a
21 high-throughput and high-density arrayed DPN writing system. The present
22 invention provides a nanoplotter with an array of independently active,
23 microfabricated, closely spaced DPN probes.

1 FIG. 2 shows a schematic view of an active multi-pen, parallel
2 DPN writing system 32 according to one type of embodiment of the current
3 invention. A DPN probe chip 34 having a probe array including a plurality of
4 active probes 38 is mounted on an AFM scanner tube 40 in a manner similar to
5 standard single-tip AFM probes. AFM feedback electronics 42, typically piezo
6 tube electronics, control horizontal and vertical movement of the probe chip 34.

7 As the tips 20 of the active probes 38 are in contact with the
8 writing substrate 24, an integrated actuator 46 controlled by a connected
9 auxiliary control circuit 48 directs individual movement of the tips, preferably
10 while the probe chip 34 is raster-scanned along the substrate 24 for patterning.
11 The location of the integrated actuator 46 indicated in FIG. 2 is illustrative, and
12 other actuator locations are contemplated. The term “in contact” is intended to
13 refer to a sufficient proximity between the tips 20 and the substrate 24 to allow
14 patterning of the patterning compound 26. When supplied with current or
15 voltage from the control unit 48 via the probe chip 34, the actuator 46 moves a
16 cantilever 50 of the active probe 38 to lift the tip 20 at an end of the cantilever
17 off the writing substrate 24. This suspends the chemical deposition process. In
18 this way, the active probe 38 can be individually controlled through selective
19 application of current or voltage to create arbitrary patterns with high
20 throughput.

21 FIGs. 3a and 3b show an array 56 of thermally actuated probes
22 54 according to a preferred type of embodiment of the present invention, before
23 and after actuation of selected probes, respectively. In FIG. 3a, the array 56 is

1 shown having five thermally actuated probes 54, none of which is actuated. In
2 response to an applied current, and as shown in FIG. 3b, the second and fourth
3 thermally actuated probes (indicated by arrows) are flexed upwardly (in FIGs.
4 3a and 3b, into the paper), thus moving their tips 20 away from the writing
5 substrate 24, and suspending chemical deposition. It will be appreciated by
6 those skilled in the art that the selective distribution of current to form the
7 patterns 28 may be controlled by programming the control circuit 48.

8 The material of the cantilever beam 50 in the thermally actuated
9 probes 54 preferably is silicon nitride thin film formed by low pressure
10 chemical vapor deposition methods (LPCVD). According to a preferred type
11 of method of the present invention, the thermally actuated probes 54 are
12 formed by creating silicon nitride probes that include a thermal actuator having
13 at least a resistive heater 66.

14 FIGs. 4a and 4b show one of the thermally actuated probes 54 in
15 non-flexed and flexed (actuated) positions, respectively. The resistive heater
16 66, patterned onto the silicon nitride cantilever 50 of the thermally actuated
17 probe 54, is coupled to a bonding wire 70 for carrying current to the resistive
18 heater. The bonding wire 70 is in turn coupled to the control circuit 48 for
19 selectively distributing current to the bonding wire 70 and thus actuating the
20 thermally actuated probes 54. Preferably, a metal film patch 68 is connected to
21 the cantilever 50 to increase the deflection of the probe 54.

22 FIGs. 5a-5e and 6a-6d show formation steps for the thermally
23 actuated probe array 56, forming a single thermally actuated probe 54 and a

1 pair of thermally actuated probes, respectively. Referring to FIG. 5a, a silicon
2 dioxide thin film 60 is grown on a front side of a silicon substrate 62,
3 preferably a <100>-oriented silicon wafer, to form a protective mask for
4 creating the tip 20. The oxide layer 60 is patterned photolithographically to
5 realize the mask for forming the tip 20. In FIG. 5b (also in FIG. 6a), a portion
6 of the silicon substrate 62 defining the pyramidal shape of the tip 20 is formed
7 by using anisotropic wet etching in ethylene diamine pyrocatechol (EDP).
8 Next, as shown in FIG. 5c (6b), a layer of LPCVD silicon nitride 64 is
9 deposited and patterned onto the etched silicon substrate 62 to define the shape
10 of the thermally active probe 54, including the cantilever 50. As shown in
11 FIGs. 5d (6c), the resistive (ohmic) heater 66 and the (optional) metal patch 68
12 are formed on the thermally active probe 54 by depositing and patterning, for
13 example, Cr/Au onto the layer of silicon nitride 64, creating an integrated
14 bimetallic thermal actuator. The thermally actuated probes 54 are then released
15 by using EDP etching to undercut the support substrate 62. A portion of a
16 silicon substrate 62 provides a handle for the thermally actuated probes 54, as
17 shown in FIGs. 4a and 4b.

18 In operation, the thermally actuated probes 54, in response to an
19 applied current, bend along their length to move the tip 20 as shown in FIG. 4b,
20 due to differential thermal expansion of the metal for resistive heater 66 and
21 optional patch 68 and the cantilever 50 of the thermally actuated probe. In a
22 preferred method of operation, the control circuit 48 sends a current through
23 the bonding wire 70 to the resistive heater 66 to bend the thermally actuated

probe 54 into a circular arc of radius R due to differential thermal expansion of the silicon nitride cantilever 50 and the gold patch 68.

The expression for R under a given temperature change of ΔT is approximately $R = -\frac{(w_1 E_1 t_1^2)^2 + (w_2 E_2 t_2^2)^2 + 2w_1 w_2 E_1 E_2 t_1 t_2 (2t_1^2 + 3t_1 t_2 + 2t_2^2)}{6w_1 w_2 E_1 E_2 t_1 t_2 (t_1 + t_2)(\alpha_1 - \alpha_2)\Delta T}$.

The parameters w , t , E and α , respectively, are the width, thickness, Young's modulus of elasticity, and the coefficient of thermal expansion of two constituent materials, denoted as materials 1 and 2. The subscripts correspond to these two materials. The temperature of a thermal actuator is dictated by the heat balance of the beam. Heat is generated by ohmic heating and lost through conduction and convection.

In the thermally actuated probe 54, the bending of the cantilever beam 50 results in a deflection of the tip 20 of δ :

$$\delta = R \left(1 - \cos \left(\frac{L}{R} \right) \right)$$

Accordingly, application of current I through selected bonding wires 70 causes the cantilever 50 of the thermally actuated probes 54 connected to the bonding wires to deflect upwardly and thus move the tip 20, as shown in FIG. 4b.

The throughput of probe-based nanolithography can be made very high when a large number of active probes 38 in parallel are integrated on the probe chip 34. The thermally actuated probe array 36, manufactured according to the preferred type of embodiment of the present invention

1 described above, results in a compact nanoplotter with high probe densities
2 (spaced 100 μm on center) and integrated sharp tips, and may be used for
3 nanolithography and AFM imaging.

4 According to another preferred type of embodiment of the present
5 invention, an electrostatically actuated probe 72, shown in a preferred type
6 embodiment in FIG. 7, is provided. Preferably, the probe 72 is formed as a unit
7 of an electrostatic probe array 74, shown in a preferred embodiment in FIG. 8
8 in combination with the probe chip 34.

9 As shown in FIGs. 7 and 8, the electrostatically actuated probe 72
10 includes an electrostatic actuator 76, which may include a paddle-shaped plate
11 78 at the inner longitudinal end of the cantilever 50, longitudinally opposite to
12 the tip 20. The paddle-shaped plate 78 is preferably integrally formed with the
13 electrostatically actuated probe 72. The electrostatic actuator 76 further
14 includes a counter electrode 81, which is preferably stationary, and may be
15 formed on the probe chip 34, for electrostatically interacting with the paddle-
16 shaped plate 78. The counter electrode 81 may be formed as part of a parallel
17 array of electrodes electrically connected to a number of bonding pads 85
18 longitudinally opposed to the counter electrodes, and both are patterned,
19 adhered, or otherwise formed or attached to a glass substrate 94 which, in the
20 completed embodiment, covers the array of counter electrodes and connecting
21 bonding pads. The bonding pads 85 are preferably electrically connected to the
22 control circuit 48 for selectively applying a voltage to one or more of the
23 bonding pads. Methods for manufacturing the glass layer 94 including the

1 counter electrodes 81 and the bonding pads 85 will be apparent to those in the
2 art.

3 It is preferred that the electrostatically actuated probe 72 is also
4 supported at or near the midpoint of the cantilever 50 by a compact, soft spring
5 80, for providing torsion support to the electrostatically actuated probe,
6 allowing deflection and thus angular motion of the probes, for moving the tips
7 20 of the probes. As shown in FIG. 8, the spring 80 for each of the array 74 of
8 electrostatically actuated probes 72 is preferably a section of a unitary piece
9 (such as a twist beam) laterally extending through each individual probe. It is
10 further preferred that each section of the spring 80 have a relatively small cross
11 section along the longitudinal direction of the cantilever 50. As one in the art
12 will appreciate, dimensions of the spring 80 such as the cross-sectional area can
13 be varied depending on boundary conditions to control the angular flexibility of
14 the cantilever 50.

15 FIG. 9 is a top view of a preferred embodiment of the
16 electrostatically actuated probe 72. It is preferred, though not required, that the
17 cantilever 50, paddle-shaped plate 78, and soft spring 80 be formed integrally
18 from boron-doped silicon. This material is preferred both for its low etch rate
19 in EDP solutions and for its relatively high electrical conductivity.

20 A preferred method of fabrication of the electrostatically actuated
21 probe 72 is shown in FIGs. 10a-10f. Referring first to FIG. 10a, a silicon
22 dioxide layer 82 is grown on a front side of a three-layered wafer containing a
23 heavily boron-doped silicon layer 84 sandwiched between a <100>-oriented

1 silicon wafer 86 and an epitaxial <100>-oriented silicon layer 88.
2 Alternatively, the silicon layer 84 may be doped by phosphorous. The silicon
3 dioxide layer 82 defines boundaries of a mask for forming the tip 20.
4 Furthermore, the silicon dioxide layer 82 can define boundaries for forming a
5 spacer 90, which vertically separates the electrostatically actuated probe 72
6 from the counter electrode 81, which is patterned on a separate glass substrate
7 94. In FIG. 10b, the silicon tip 20 and the spacer 90 are formed from the
8 epitaxial silicon wafer 88 by EDP etching. Next, as shown in FIG. 10c, a
9 thermal oxide layer 92 is grown over the epitaxial silicon wafer 88, including
10 the tip 20, the spacer 90, and the boron-doped silicon layer 84 to protect the
11 front side during the final release. As shown in FIG. 10d, the silicon wafer 86
12 is then etched by EDP to remove material underneath the boron-doped silicon
13 layer 84, and release the boron-doped silicon cantilever 50.

14 Next, as shown in FIG. 10e, the thermal oxide layer 92 is
15 removed, and the electrostatically actuated probes 84 are formed from the
16 boron-doped silicon layer 84, including, preferably integrally, the cantilever 50,
17 the soft spring 80, and the paddle-shaped plate 78, for each probe in the array.
18 As shown in FIG. 8, the portion of the cantilever 50 longitudinally disposed
19 between the paddle-shaped plate 78 and the soft spring 80 is preferably wider
20 in cross-sectional area along the lateral direction, i.e. in the direction of the
21 length of the soft spring, than the distal portion of the cantilever. In this way,
22 the deflection of the tip 20 is greater because the bending torque is fully

1 transferred to the support spring 80. The electrostatically actuated probe 72 is
2 released.

3 Finally, as shown in FIG. 10f, the layer of glass 94 and the
4 connected counter electrode 81 are formed or placed over the spacer 90.

5 The preferred fabrication method results in electrostatically
6 actuated probes 72 having a sharp tip 20 (preferably, <100 nm radius of
7 curvature) and spaced approximately 620 μm on center. Accordingly,
8 electrostatically actuated probes 72 according to a preferred embodiment of the
9 present invention can be used for both DPN writing and AFM imaging.

10 Bonding wires 70 (not shown in FIGs. 10a-10f) preferably
11 connect the paddle-shaped plate 78 to ground potential, while the counter
12 electrode 81 is preferably electrically coupled to the control circuit 48 via
13 bonding pads 85 for applying voltage to the counter electrode. It will be
14 appreciated that the electric potentials of the paddle-shaped plate 78 and the
15 counter electrode 81 may alternatively be reversed; i.e. the paddle-shaped plate
16 may be coupled to a voltage source, while the counter electrode may be
17 grounded. The modifications necessary for such an alternative embodiment
18 will be understood by those in the art.

19 In a preferred method of operation, voltage is applied to the
20 paddle-shaped plate 78 to apply potential to the paddle-shaped plate 78, while
21 the conductive counter electrode 81 is grounded. Again, alternatively, the
22 voltage application and grounding functions could be reversed between the top
23 electrode 81 and the paddle-shaped plate 78. Either operation applies a

1 differential electrical voltage across the top electrode 81 and the paddle-shaped
2 plate 78, which are preferably separated by the spacer 90. An attractive force
3 develops between the plates of the counter electrode 81 and the paddle-shaped
4 plate 78 that pulls them toward each other, thus tilting the cantilever 50, and
5 preferably angularly deflecting the cantilever 50 about the soft spring 80, to
6 move the tip 20 away from the substrate 24. As in the thermally actuated
7 probes 54, the tip 20 can thus be selectively lifted to suspend the writing (or
8 imaging) process.

9 A number of preferred embodiments have been described for
10 active, one-dimensional arrays. However, arrays are possible in two
11 dimensions as well. FIG. 12 shows a two-dimensional array 100 according to
12 another preferred embodiment of the present invention. The two-dimensional
13 array 100 shown in FIG. 12 includes six rows and five columns of
14 downwardly-angled probes 102. The downwardly-angled probes 102 may be
15 produced by, for example, modifying the formation process for the thermally
16 actuated probe array 56 to extend cantilevers of individual, thermally actuated
17 probes 54 from cavities (replicated cells) that are preferably evenly disposed
18 along the two-dimensional array 100. The thermally actuated probes 54 are
19 preferably integrated into the two-dimensional array 100 due to a shorter
20 required length for each cantilever 50. The methods for modifying steps of
21 fabrication and operation for the thermally actuated probes 54 in the two-
22 dimensional array 100 will be understood by those in the art.

1 One skilled in the art can appreciate that several inventive
2 devices and methods for DPN arrays have been shown and described, which
3 have various attributes and advantages. By configuring each probe to be
4 individually addressed and actuated by application of current or voltage, either
5 thermally or electrostatically, the active probe arrays according to embodiments
6 of the present invention allow the formation of arbitrary patterns with added
7 resolution, at throughput comparable to conventional methods.

8 While various embodiments of the present invention have been
9 shown and described, it should be understood that other modifications,
10 substitutions and alternatives are apparent to one of ordinary skill in the art.
11 Such modifications, substitutions and alternatives can be made without
12 departing from the spirit and scope of the invention, which should be
13 determined from the appended claims.

14 Various features of the invention are set forth in the appended
15 claims.